

# SPLC 2016

The 20th International Systems and  
Software Product Line Conference

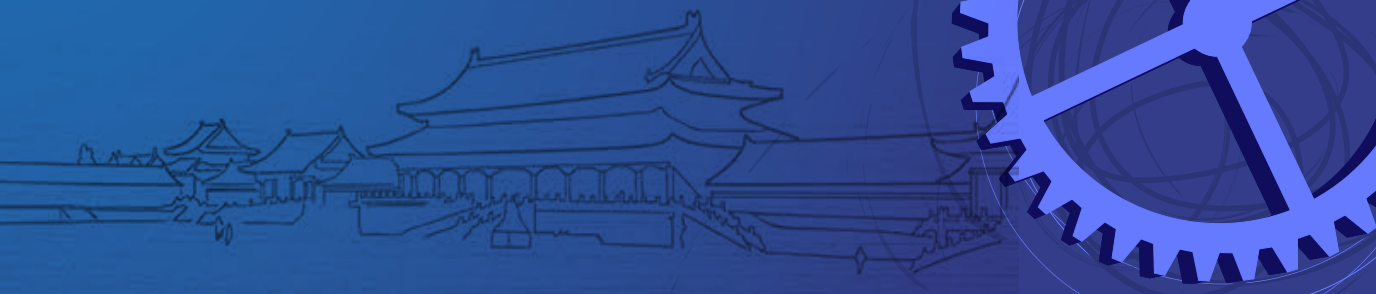
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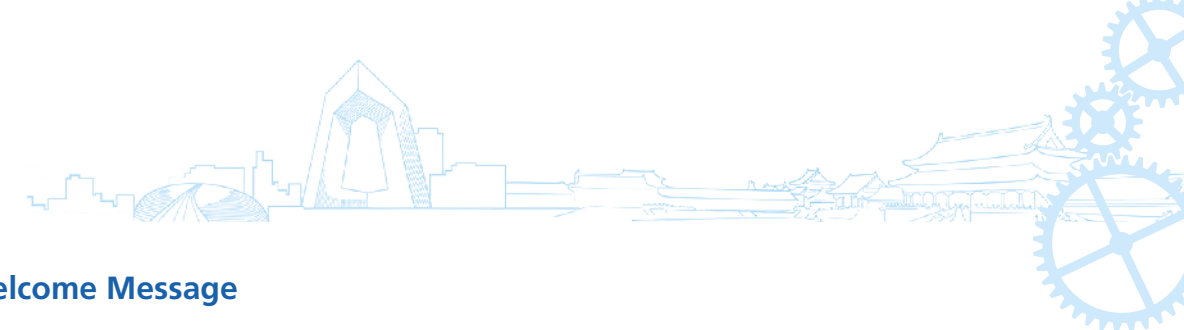
September 19-23, 2016 Beijing, China

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## Program





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## Welcome Message

Welcome to SPLC 2016, the 20th International Systems and Software Product Line Conference. SPLC is the premier forum for practitioners, researchers and educators to present and discuss the most recent ideas, innovations, trends, experiences, and concerns in the area of software product lines, software product family engineering and, more in general, systems family engineering.

SPLC 2016 consists of a variety of exciting events, including keynotes, tutorials and paper presentations. This year, the conference consists of a broad combination of research, software industry, vision and system engineering papers and tool demos, as well as a doctoral symposium, workshops and tutorials. Totally, the conference received 79 paper submissions: 44 research papers, 8 software industry papers, 7 vision papers, 4 systems engineering papers and several tool demos and doctoral proposals. We would like to thank all authors for submitting papers.

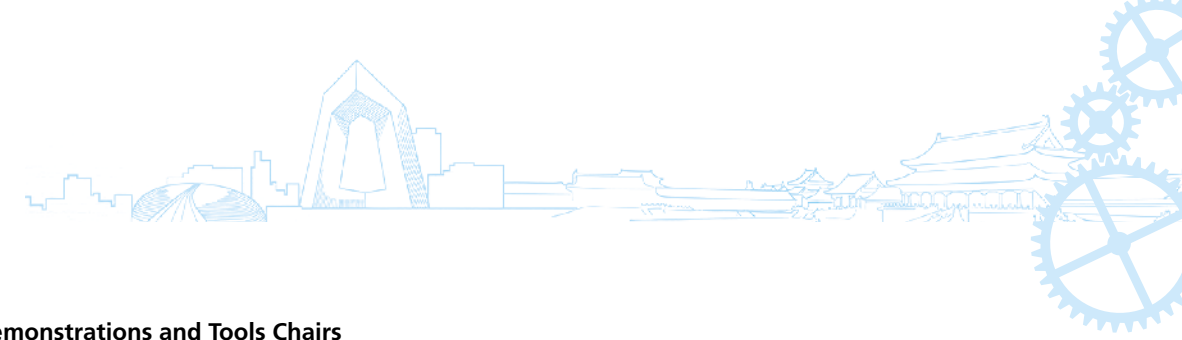
Each submitted paper was reviewed by at least three Program Committee members. Based on the review reports and intensive discussions conducted electronically, the Program Committee selected 17 full papers and 5 short ones for the research track, 5 full papers and 1 short paper for the software industry track, one short paper for the vision track, 3 full papers for the systems engineering track, leaving us with a paper acceptance rate of 39%, 63%, 14% and 75%, respectively.

In addition, many recent excellent tools used in research and industry were shown in the tool demo track, and a great deal of new ideas were discussed at the doctoral symposium, workshops and tutorials.

We would like to thank our keynote speakers, Hans van Vliet, Tetsuo Tamai, Huaimin Wang, and Dayong Jiang for sharing their ideas and insights on this field. Also, we would like to thank the Program Committee members for their help in making the selection of the papers. We also would like to thank the members of the Organizing Committee whose efforts contributed to make the conference a success. Finally, we would like to thank our sponsors for their support and contributions.

Sincerely,

SPLC 2016 Organization Committee



## Organization

### General Chairs

Hong Mei Shanghai Jiao Tong University, China

### Research Track Chairs

Rick Rabiser Johannes Kepler University Linz, Austria  
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### Software Industry Track Chairs

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### Hall of Fame Chairs

Goetz Botterweck Lero, Ireland  
Natsuko Noda Shibaura Institute of Technology, Japan

### Workshop Chairs

Andrzej Wąsowski IT University, Denmark  
Xin Peng Fudan University, China

### Tutorial Chairs

Jocelyn Simmonds University of Chile, Chile  
Jun Wei Chinese Academy of Sciences, China

### Demonstrations and Tools Chairs

Thorsten Berger Chalmers Univ. of Technology, Sweden  
Norbert Siegmund University of Passau, Germany

### Doctoral Symposium Chairs

Li Zhang Beihang University, China  
Ebrahim Bagheri Ryerson University, Canada

### Publicity Chairs

David Benavides University of Seville, Spain  
Leopoldo Teixeira Federal Univ. of Pernambuco, Brazil  
Jiangtao Wang Peking University, China

### Local Chair

Dan Hao Peking University, China

### Publication/Proceedings Chair

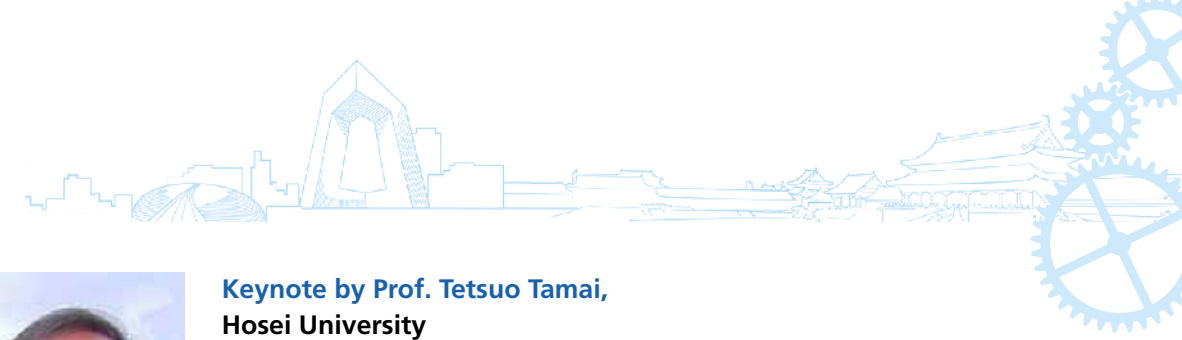
Yingfei Xiong Peking University, China

### Financial Chair

Junfeng Zhao Peking University, China

## Steering Committee

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Hong Mei Shanghai Jiao Tong University, China



## Keynotes



### Keynote by Prof. Hans van Vliet, VU University Amsterdam

#### Decision making in software architecture

##### Abstract:

Traditionally, software architecture is seen as the result of the software architecture design process, the solution, usually represented by a set of components and connectors. Recently, the why of the solution, the set of design decisions made by the software architect, is complementing or even

replacing the solution-oriented definition of software architecture. This in turn leads to the study of the process of making these decisions. How do people take design decisions? Is that a rational process, whereby people choose the best option at each stage, or is the process a lot more flimsy? In this talk, I explore this and other questions about the nature of the software architecture design process. I also outline some research directions that may help us understand and improve the software architecture design process.

##### Bio:

Hans van Vliet is Professor in Software Engineering at the VU University Amsterdam, The Netherlands, since 1986. He got his PhD from the University of Amsterdam. His research interests include software architecture, knowledge management in software development, global software development, and empirical software engineering. Before joining the VU University, he worked as a researcher at the Centrum voor Wiskunde en Informatica (CWI, Amsterdam). He spent a year as a visiting researcher at the IBM Almaden Research Center in San Jose, California. He is the author of "Software Engineering: Principles and Practice", published by Wiley (3rd Edition, 2008). He is a member of IFIP Working Group 2.10 on software architecture, and the Editor in Chief of the Journal of Systems and Software.



### Keynote by Prof. Tetsuo Tamai, Hosei University

#### Product-centered View vs Process-centered View

##### Abstract:

Looking back the history of software engineering, we can observe an alternating cycle of interest on product-centered view vs. process-centered view in software research and practices. From the late 1980's to early 1990's, software process became quite an active field. Activities concerning software

process were hot in academia as well as in industry. The interest on software process saw its peak in early 1990's but lost the momentum soon. Then came the fever on software architecture. The book "Software Architecture" by M. Shaw and D. Garlan was published in 1996 and widely read. Design patterns and application frameworks drew attention about the same time, which together indicate a shift of interest from process to product. In 2000's, the interest on processes revived. One phenomenon is the upsurge of interest on the agile process. As software product lines (SPL) contains the word product in the term, it deals with a variety of products but its focus is also on the process of managing a collection of similar software production lines. In this talk, I'd like to give a perspective on the alternating cycle of interest on product-centered view vs. process-centered view and then characterize SPL in this framework.

##### Bio:

Tetsuo Tamai received the B.S., M.S. and Dr.S. degrees in mathematical engineering from the University of Tokyo.

He joined Mitsubishi Research Institute, Inc. in April 1972 and had been the manager of Artificial Intelligence Technologies Section from October 1985 to March 1989. He became Associate Professor of Graduate School of Systems Management, the University of Tsukuba in 1989. He then became Professor of Graduate School of Arts and Sciences, the University of Tokyo in 1994. He retired from the University of Tokyo in March 2012 and moved to Faculty of Science and Engineering, Hosei University in April 2012. His current research includes requirements engineering, high reliability component-based software engineering, collaboration and role modelling, formal analysis of software architectures and software evolution process.

He has been contributing to the activities of Japan Society for Software Science and Technology for a long time as a board member and as the Editor-in-Chief of its journal "Computer Software." He served as the Program Chair of JSSST 20th anniversary conference in September 2003. He was an associate editor of ACM Transactions on Software Engineering and Methodology (TOSEM) in 2004-2008, an associated editor of IEEE Transactions on Software Engineering (TSE) in 2008-2013, and on the editorial board of Information and Software Technology (Elsevier) since 1995. He was a member of the executive committee of ACM SIGSOFT as an International Liaison from 2001 to 2008 and also a past chair of

Special Interest Group on Software Engineering, Information Processing Society of Japan and a past chair of the Software Engineers Association, Japan.

He has been sharing responsibilities of a number of international academic conferences, serving Program Committees of ICSE's, RE's, ESEC/FSE's, ICSM's and many others and Steering Committee of APSEC, ICFEM and IWPSE. In 2008, he was Program Chair of the 16th IEEE International Conference on Requirements Engineering held in Barcelona, Spain.



**Keynote by Prof. Huaimin Wang,  
National University of Defense Technology**

**TRUSTIE: Towards Software Production based on Crowd wisdom**

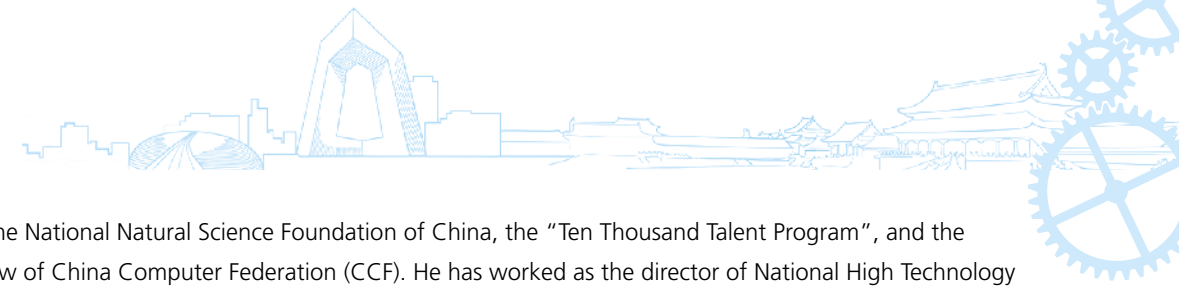
**Abstract:**

Software development is either creation activities that rely on developers' creativity and talents, or manufacturing activities that follow the engineering processes. Engineering processes need to include creation activities to address tasks such as requirement elicitation and bug finding. On the other hand, by

exploiting the crowd wisdom, open-source development demonstrated to be a suitable environment for software creation. However, it also has several limitations, such as guaranteeing the progress and quality of production process. This talk will introduce a software development platform and ecosystem that combines the strengths of the two models. First, we propose the Trustworthy Software Model as a basis to support such a hybrid development ecosystem. The core of this model contains a novel lifecycle model, an evidence model and an evolution model. Second, based on the model, we propose the Trustworthy Software Development and Evolution Service Model. It integrates from crowd collaboration, resource sharing, runtime monitoring, and trustworthiness analysis into an integrated framework. Based on this integrated model, we designed and implemented TRUSTIE, which enables crowd-oriented collaboration among internal development teams and the external crowds by combining the software creation and software manufacturing in one ecosystem.

**Bio:**

Huaimin Wang received his Ph.D. in Computer Science from National University of Defense Technology (NUDT). He is now a professor. His current research interests include distributed computing technologies and systems, crowd-based software engineering and trustworthy software. He has been awarded the "Chang Jiang Scholars Program" professor by Ministry of Education of China, the Distinct Young Scholar



by the National Natural Science Foundation of China, the "Ten Thousand Talent Program", and the fellow of China Computer Federation (CCF). He has worked as the director of National High Technology Research and Development Program of China, and chaired more than 20 research projects. He has published more than 200 research papers in international conferences and journals. He was program co-chair of COMPSAC'2003, SRDS'2006-2007, SOSE'2013, IVCE'2009-2013, CNCC'2013, and invited to give keynotes in many important conferences and forums. He was on the Editorial Board of Journal of Software, Science Communications, Journal of Computer Science and Technology, Journal of Computer Research and Development.



**Keynote by Dayong Jiang,  
Vice President of IT R&D Dept, Huawei Technologies Co. Ltd.**

**Software Development Trend and Practice in the Cloud Era**

**Abstract:**

Now we are in the cloud era. The cloud architecture fully supports the digital transformation of all industry. Software development faces new challenges, such as business agility requirements (weeks level), the sharp increase in the scale of software source code (tens of millions of rows), ecological

management issues, compatibility issues of open source, etc. How to enhance the efficiency of software development and tools enablement, it will be a challenge to products and solutions providers. Huawei will share how to innovate and practices in these technology trends.

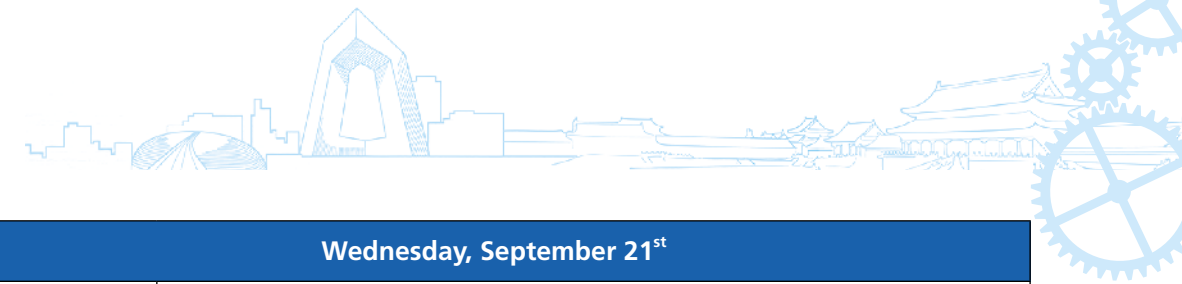
**Bio:**

Mr Jiang joined Huawei in 1999 and have been worked in the R&D area for 16 years.

He involved in and was responsible for the products & solutions of telecommunications and information and technology, such as A8010, UMG8900, DC<sup>2</sup> and so on.

He was in charge of R&D projects that thousands of people involved in per month. He has played a role of engineer, project manager, test manager, project leader, line manager, product development unit director, develop unit manager, quality director, VP of R&D and so on. And he has an understanding of quality management, project management, team management and R&D model.



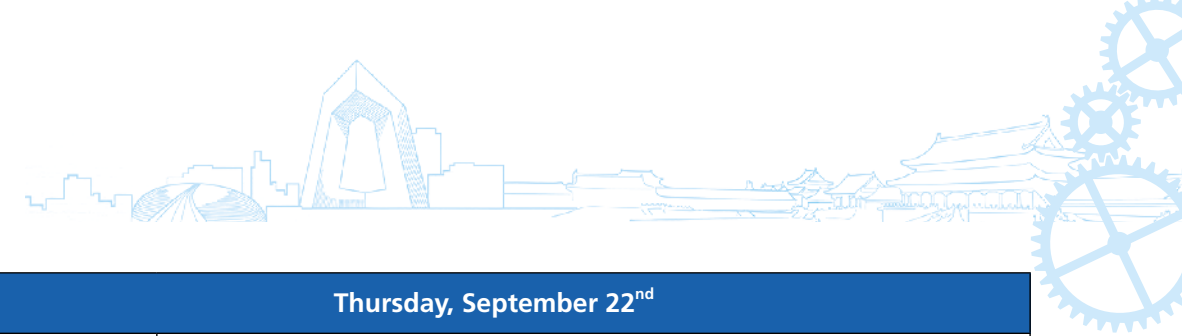


## Conference Program

Monday, September 19 <sup>th</sup>	
9:00-12:00	<b>REVE workshop</b> (Room 5) <b>Tutorial 1: Managing requirements in product lines</b> (Room 1) <i>Darilo Beuche</i>
12:00-14:00	Lunch (Restaurant, Ground Floor, Friendship Palace)
14:00-17:00	<b>REVE workshop</b> (Room 5) <b>Tutorial 2: Leveraging model driven engineering in software product line architectures</b> (Room 1) <i>Bruce Trask</i>

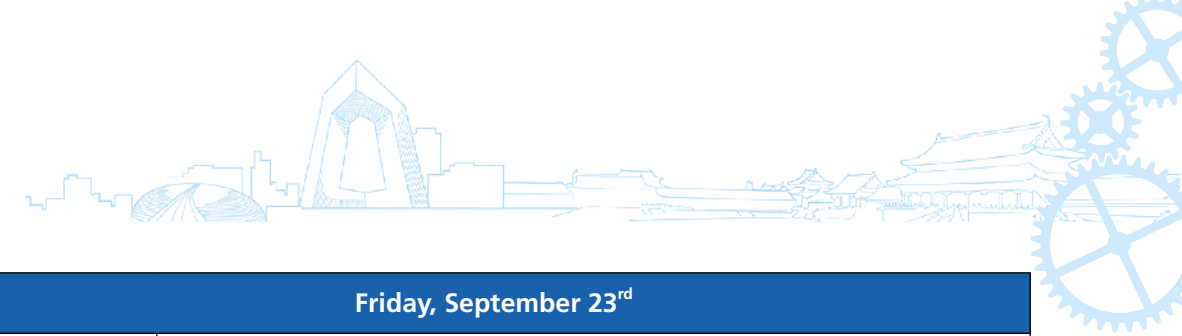
Tuesday, September 20 <sup>th</sup>	
9:00-12:00	<b>Tutorial 3: EASy-Producer: From product lines to variability-rich software ecosystems</b> (Room 1) <i>Klaus Schmid</i> <b>Tutorial 4: Software reuse and reusability based on requirements: product lines, cases and feature-similarity models</b> (Room 5) <i>Mike Mannion</i> <b>Doctoral Symposium</b> (Room 3) <ol style="list-style-type: none"> <li><i>Bo Wang</i>. Dynamic Analysis of Shared Execution in Software Product Line Testing.</li> <li><i>Muhammad Javed</i>. A Framework for Enhanced Feature Models based on Mathematical Analysis.</li> <li><i>Laurens Sion</i>. Towards Systematically Addressing Security Variability in Software Product Lines.</li> </ol>
12:00-14:00	Lunch (Restaurant, Ground Floor, Friendship Palace)
14:00-17:00	<b>Tutorial 3: EASy-Producer: from product lines to variability-rich software ecosystems</b> (Room 1) <i>Schmid, Eichelberger</i> <b>Tutorial 5: Clean Your Variable Code with FeatureIDE</b> (Room 5) <i>Thomas Thüm</i>

Wednesday, September 21 <sup>st</sup>	
8:30-9:00	<b>Opening speech by the general chair</b> (Room 5) <b>Joint speech by the main track chairs</b> (Room 5)
9:00-10:20	<b>Keynote 1: Decision making in software architecture</b> (Room 5) <i>Hans van Vliet</i> Chair: <i>Rick Rabiser</i>
10:20-10:40	Coffee Break
10:40-12:00	<b>Keynote 2: Software Development Trend and Practice in the Cloud Era</b> (Room 5) <i>Dayong Jiang</i> Chair: <i>Bing Xie</i>
12:00-14:00	Lunch (Restaurant, Ground Floor, Friendship Palace)
14:00-15:30	<b>Session 1: Empirical Studies</b> (Room 5) Chair: <i>Holger Eichelberger</i> <ol style="list-style-type: none"> <li><i>Gabriel Ferreira, Momin Malik, Christian Kaestner, Juergen Pfeffer, Sven Apel</i>. Do #ifdefs Influence the Occurrence of Vulnerabilities? An Empirical Study of the Linux Kernel.</li> <li><i>Varvana Myllaerniemi, Mikko Raatikainen, Juha Savolainen, Tomi Mannisto</i>. Purposeful Performance Variability in Software Product Lines: A Comparison of Two Case Studies.</li> <li><i>Li Li, Jabier Martinez, Tewfik Ziadi, Tegawend F. Bissyand, Jacques Klein and Yves Le Traon</i>. Mining Families of Android Applications for Extractive SPL Adoption.</li> </ol> <b>Session 2: Industrial Applications</b> (Room 2+Room 3) Chair: <i>Rick Rabiser</i> <ol style="list-style-type: none"> <li><i>Philipp Kehrbusch, Johannes Richenhagen, Bernhard Rumpel, Axel Schloßer, Christoph Schulze</i>. Interface-Based Similarity Analysis of Software Components for the Automotive Industry.</li> <li><i>Alcemir Santos, Ivan Machado, Eduardo Almeida</i>. RiPLE-HC: JavaScript Systems Meets SPL Composition.</li> <li><i>Darilo Beuche, Michael Schulze, Maurice Duvigneau</i>. When 150% Is Too Much: Supporting Product Centric Viewpoints In An Industrial Product Line.</li> </ol>
15:30-16:00	Coffee Break



16:00-17:30	<p><b>Session 3: Variability Modeling and Management</b> (Room 2+Room3) Chair: <i>Li Zhang</i></p> <ol style="list-style-type: none"> <li><i>Damir Nesic, Mattias Nyberg</i>. Multi-view modelling and automated analysis of product line variability in systems engineering.</li> <li><i>Paul Temple, Jose Angel Galindo Duarte, Mathieu Acher, Jean-Marc Jezequel</i>. Using Machine Learning to Infer Constraints for Product Lines.</li> <li><i>Maya Retno Ayu Setyautami, Reiner Haehnle, Radu Muschevici, Ade Azurat</i>. A UML Profile for Delta-Oriented Programming to Support Software Product Line Engineering.</li> <li><i>Sebastian Krieter, Reimar Schröter, Thomas Thüm, Wolfram Fenske, Gunter Saake</i>. Comparing Algorithms for Efficient Feature-Model Slicing.</li> </ol> <p><b>Session 4: Maintenance and Evolution</b> (Room 5) Chair: <i>Daniela Rabiser</i></p> <ol style="list-style-type: none"> <li><i>Thomas Thüm, Marcio Ribeiro, Reimar Schröter, Janet Siegmund, Francisco Dalton</i>. Product-Line Maintenance with Emergent Contract Interfaces.</li> <li><i>Gabriela Sampaio, Paulo Borba, Leopoldo Teixeira</i>. Partially Safe Evolution of Software Product Lines.</li> <li><i>Konstantinos Plakidas, Srdjan Stevanetic, Daniel Schall, Tudor B. Ionescu, Uwe Zdun</i>. How do software ecosystems evolve? A quantitative assessment of the R ecosystem.</li> </ol>
	17:45-

Thursday, September 22 <sup>nd</sup>	
9:00-10:20	<p><b>Keynote 3: TRUSTIE: Towards Software Production based on Crowd wisdom</b> (Room 5) <i>Huaimin Wang</i> Chair: <i>Christoph Elsner</i></p>
10:20-10:40	Coffee Break
10:40-12:00	<p><b>Keynote 4: Product-centered View vs Process-centered View</b> (Room 5) <i>Tesuo Tamai</i> Chair: <i>Yun Xie</i></p>
12:00-14:00	Lunch (Restaurant, Ground Floor, Friendship Palace)
14:00-15:30	<p><b>Session 5: Analysis</b> (Room 2+Room 3) Chair: <i>David Benavides</i></p> <ol style="list-style-type: none"> <li><i>Rafael Olaechea, Uli Fahrenberg, Joanne Atlee, Axel Legay</i>. Long-Term Average Cost in Featured Transition Systems.</li> <li><i>Jrôme Le Noir, Sbastien Madelnat, Grgory Gailliard, Christophe Labreuche, Mathieu Acher, Olivier Barais, Olivier Constant</i>. A Decision-making Process for Exploring Architectural Variants in Systems Engineering.</li> <li><i>Yi Zhang, Jianmei Guo, Eric Blais, Krzysztof Czarnecki, Huiqun Yu</i>. A Mathematical Model of Performance-Relevant Feature Interactions.</li> </ol> <p><b>Session 6: Software Quality Assurance</b> (Room 5) Chair: <i>Yingfei Xiong</i></p> <ol style="list-style-type: none"> <li><i>Laurens Sion, Dimitri Van Landuyt, Wouter Joosen, Gjalt de Jong</i>. Systematic Quality Trade-off Support in the Software Product-Line Configuration Process.</li> <li><i>Anatoly Vasilevskiy, Franck Chauvel, Oystein Haugen</i>. Toward Robust Product Realisation in Software Product Lines.</li> <li><i>Aitor Arrieta, Shuai Wang, Goiuria Sagardu, Leire Etxeberria</i>. Search-Based Test Case Selection of Cyber-Physical System Product Lines for Simulation-Based Validation.</li> </ol>
15:30-16:00	Coffee Break



16:00-17:30	<p><b>Session 7: Reuse</b> (Room 2+Room 3) Chair: Tomi Männistö</p> <ol style="list-style-type: none"> <li>1. Raul Lapena, Manuel Ballarin, Carlos Cetina. Towards Clone-And-Own Support: Locating Relevant Methods in Legacy Products.</li> <li>2. Daniela Rabiser, Paul Gruenbacher, Herbert Praehofer, Florian Angerer. A Prototype-based Approach for Managing Clones in Clone-and-Own Product Lines.</li> <li>3. Mike Mannion, Juha Savolainen. Choosing Reusable Software Strategies.</li> <li>4. Jose A. Galindo, Mathieu Acher, Juan M. Tirado, Cristian Vidal, Benoit Baudry, David Benavides. Exploiting the enumeration of All Feature Model Configurations. A New Perspective with Distributed Computing.</li> </ol> <p><b>Session 8: Industrial Lessons</b> (Room 5) Chair: Bo Zhang</p> <ol style="list-style-type: none"> <li>1. Motoi Nagamine, Tsuyoshi Nakajima, Noriyoshi Kuno. A Case Study of Applying Software Product Line Engineering to the Air Conditioner Domain.</li> <li>2. Takahiro Iida, Masahiro Matsubara, Kentarou Yoshimura, Hideyuki Kojima, Kimio Nishino. PLE for Automotive Braking System with Management of Impacts from Equipment Interactions.</li> <li>3. Thomas Fogdal, Helene Scherrebeck, Juha Kuusela, Martin Becker, Bo Zhang. Ten Years of Product Line Engineering at Danfoss: Lessons Learned and Way Ahead.</li> </ol>
	18:00-

Friday, September 23 <sup>rd</sup>	
8:30-10:00	<p><b>Session 9: Architectural Analysis</b> (Room 5) Chair: Dimitri Van Landuyt</p> <ol style="list-style-type: none"> <li>1. Johannes Richenhagen, Bernhard Rumpe, Axel SchloBer, Christoph Schulze, Kevin Thissen, Michael von Wenckstern. Test-Driven Semantical Similarity Analysis for Software Product Line Extraction.</li> <li>2. Iris Groher, Rainer Weinreich, Georg Buchgeher, Robert Schossleitner. Reusable Architecture Variants for Customer-Specific Automation Solutions.</li> <li>3. Thomas Kuehn, Walter Cazzola. Apples and Oranges: Comparing Top-Down and Bottom-Up Language Product Lines.</li> </ol> <p><b>Demonstration Track</b> (Room 1)</p> <ol style="list-style-type: none"> <li>1. Tristan Pfofe. Thomas Thuem, Sandro Schulze, Wolfream Fenske, Ina Schaefer. Synchronizing Software Variants with VariantSync.</li> <li>2. Bo Zhang, Martin Becker. Supporting Product Configuration in Application Engineering Using EXConfig.</li> <li>3. Alcemir Rodrigues Santos, Ivan Machado, Eduardo Almeida. RiPLE-HC: Visual Support for Features Scattering and Interactions.</li> <li>4. Danilo Beuche. Using pure::variants Across The Product Line Lifecycle.</li> <li>5. Carla Bezerra, Jefferson barbosa, João Holanda Freires, Rossana Andrade, Jose Monteiro. DyMMer: A Measurement-based Tool to Support Quality Evaluation of DSPL Feature Models.</li> </ol>
	10:00-10:20
10:20-11:20	<p><b>Session 10: Variability Modeling II</b> (Room 5) Chair: Georg Buchgeher</p> <ol style="list-style-type: none"> <li>1. Gustavo Sousa, Walter Rudametkin, Laurence Duchien. Extending Feature Models with Relative Cardinalities.</li> <li>2. Jabier Martinez, Tewfik Ziadi, Tegawende F. Bissyande, Jacques Klein, Yves Le Traon. Name Suggestions during Feature Identification: The VariClouds Approach.</li> <li>3. Holger Eichelberger, Cui Qin, Roman Sizonenko, Klaus Schmid. Using iVML to model the topology of Big Data Processing Pipelines.</li> </ol> <p><b>Demonstration Track</b> (Room 1) Flexible time slot for demonstrations</p>
	11:35-13:30
13:30-15:30	<b>Hall of Fame</b> (Room 5)
	<p><b>Closing</b> (Room 5) <b>Handover to SPLC 2017</b> (Room 5)</p>



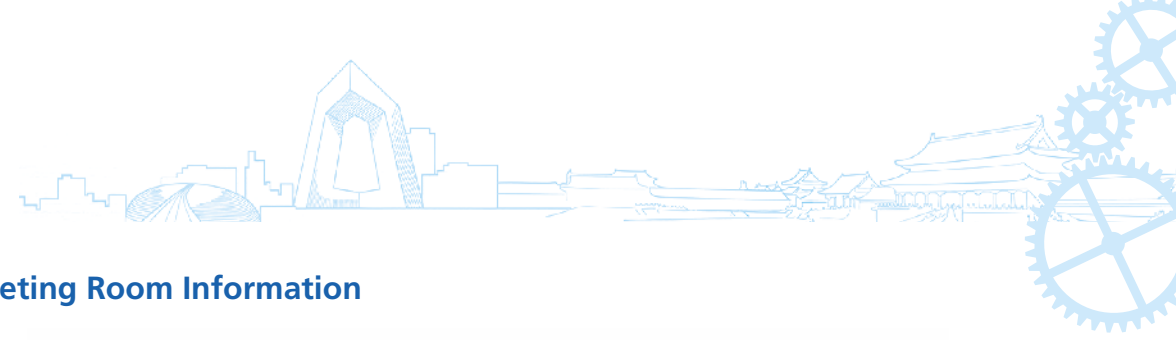
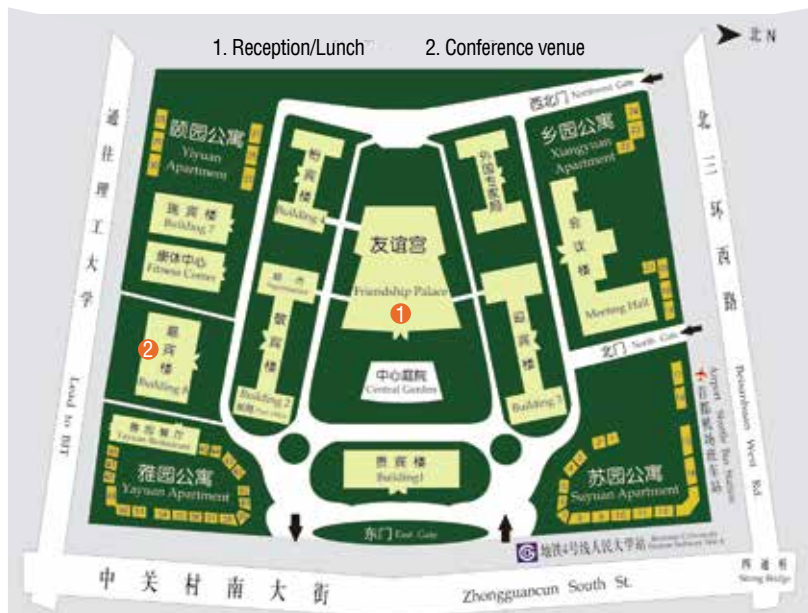
Banquet (Huajiyuan, Yuan Ming Yuan)



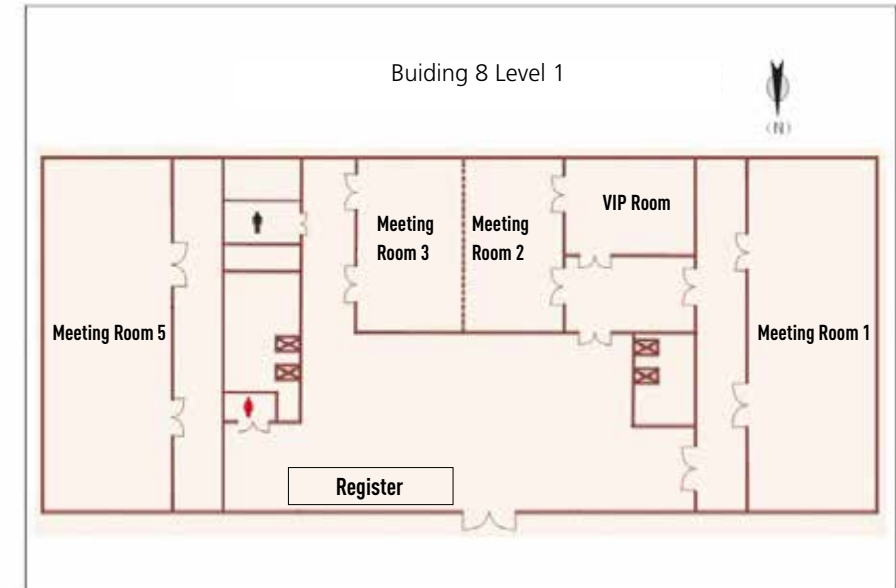
# SPLC 2016

The 20th International Systems and Software Product Line Conference

## Location



## Meeting Room Information



# SPLC 2016

The 20th International Systems and Software Product Line Conference

## Beijing

Beijing, the capital of China, is the political, cultural, domestic and international communication center of China, and is a famous, ancient historical and cultural city in the world. In 1040 B.C., Beijing was built in the current Guang'an Men, Xuanwu District, so it has cover three thousand years history. In 938 A.D., the kingdom of Liao, which regions over north China, takes Beijing (called Yanjing at that time) as the provisional capital; Beijing capital of Yuan, Ming, Qing Dynasty, Beijing has over eight hundred and fifty years history as the capital.

The long history endows Beijing with rich and intensive culture and numerous historical relics. The well-known scenic spots and historical sites are the Palace Museum (the most complex of palace in wooden structure), the elegant and magnificent Temple of Heaven, the fairyland Beihai in the real life, Guozijian, the Imperial College (the highest institution in the Yuan, Ming, and Qing Dynasty), Prince Gong's Mansion with the life track of the royal families and nobles.

The Charm of Beijing also lies in the dense and particular cultural atmosphere. The Beijing-style culture takes in every other culture to become colorful and intensive. The Charm and characteristics of Beijing are shown everywhere., including Hutong, Courtyard Houses, the Culture Street; Beijing Opera, folk arts, folk craft; food with Beijing flavor, century-old restaurant, ect. The Continuous development of CBD, the construction of the new scenes, the buildings of large business, entertainment, and consumption centers in line with the international practice enrich Beijing with flourishing and modern international flavor.

Visit Beijing Information: <http://beijing.english.china.org.cn/>

China Tours Information: <http://chinatour.net/china-tours.html>

# Welcome to SPLC 2017

General Co-chairs:  
**David Benavides, Antonio Ruiz-Cortés**

## Venue: Sevilla



